## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## In re Application of:

John Walker Todd Randazzo

Serial No.:

10/035,346

Filed:

December 18, 2001

For:

CMOS Varactor With Constant

dC/dV Characteristic

**Group Art Unit:** 

2811

Examiner:

Nadav, Ori

**Atty Docket:** 

/ 01-152/RCE/RCE

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

## SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

## Official Draftsman

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: 22 Sept 04

Respectfully submitted,

RLM

Timothy Croll

Reg. No. 36,771